

MARCH 28-29, 2024 | THESSALONIKI, GREECE



CALL FOR PAPERS

We are pleased to announce the PANhellenic Conference on Electronics and Telecommunications (PACET 2024), which will be held in Thessaloniki, Greece, on March 28-29, 2024.

Researchers, practitioners, and students from around the world are invited to submit original and unpublished papers to PACET 2024. We welcome contributions in the form of full research papers and poster presentations. Submissions should adhere to the IEEE paper format. The typical length is 4 pages, extendable to a maximum of 6 pages.

All accepted and presented papers will be published in the conference proceedings, in the IEEE Xplore Digital Library. Additionally, selected high-quality papers will have the opportunity to get published by the AEU - International Journal of Electronics and Communications Special Issue: "Advancements on Electronic Circuits and Telecommunication Systems - Selected Papers from Panhellenic Conference on Electronics and Telecommunications (PACET 2024).

Student Competition/Industry panel

PACET 2024 is committed to fostering the growth of young researchers. We invite undergraduate and postgraduate students to showcase their research work in a dedicated special student session. This session will provide an excellent opportunity for students to present their ideas, receive feedback from experts of the field, and network with their peers.

As part of the conference, a dedicated student research competition will be held to showcase outstanding research conducted by students in the field of electronics and telecommunications.

Prizes will be awarded to the top submissions of each field, providing an excellent opportunity for students to gain recognition for their work.

At the same time, students will have the opportunity to present their work to industry representatives and apply for open positions at the sponsoring companies.

Keynote Speakers

Renowned experts from academia and industry in the field of electronics and telecommunications will deliver inspiring keynote speeches, sharing their insights and experiences with the conference participants.



Prof. Dr. Georg Pelz,
Infineon Technologies AG,
Neubiberg, Germany



Prof. Andrea Massa,
IEEE Fellow, University of
Trento, Italy

The conference will also feature discussions and special sessions to enhance the learning experience and foster collaboration among participants.

Conference Tracks

- 5G and beyond 5G/6G Systems & Networks
- Analog/Mixed-Signal Circuits
- Antennas & Propagation
- Artificial Intelligence in Design Cycle Speed-up
- Big Data Processing
- CAD & Design Tools and Methodologies for IC Design
- Communication & Information Systems Security
- Computer Networks
- Digital Circuits & Systems
- Digital Signal Processing
- Edge Computing
- Embedded Systems
- Hardware Security
- Information Theory
- Information Systems
- IoT & Sensor Networks
- Low Voltage/Low Power Circuits
- mmWave & THz Communications
- Mobile Edge Computing & Edge AI
- Modelling, Simulation, Verification & Prototyping
- Nonlinear Systems & Circuits
- Optical Networks & Protocols
- Photonics & Optoelectronics
- Power Electronics
- Quantum Computing
- Radiation Hardened Circuit Design
- Reconfigurable Intelligent Surfaces & Smart Environments
- RF/mmWave Circuits & Systems
- Satellite Communication Systems
- SDN & NFV Networks
- Wireless & Mobile Computing

Conference Venue

The conference will take place in Lecture Hall III of the Aristotle University's Research Dissemination Center (KEDEA), at Aristotle University's Campus.

Important Dates

Paper Submission Opening: September 11, 2023

Paper Submission Deadline: November 17, 2023

Notification of Acceptance: January 16, 2024

Camera-Ready Submission: February 10, 2024

Conference Dates: March 28-29, 2024

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A full list of technical program committee members is available on the PACET website.

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